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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **Kunihiko NAGASE et al.**

Group Art Unit: **2891**

Application Number: **10/816,959**

Examiner: **David A. Zarneke**

Filed: **April 5, 2004**

Confirmation Number: **2428**

For: **METHOD FOR FABRICATING SEMICONDUCTOR DEVICE**

Attorney Docket Number: **042324**

Customer Number: **38834**

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

November 9, 2006

Sir:

This paper is filed in response to the Office Action dated October 18, 2006.

Amendments to the Claims begin on page 2 of this paper.

Remarks begin on page 8 of this paper.